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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yasuhito ARUGA

Application No.: 10/067,842

Filed: February 8, 2002

Docket No.: 111934

For: ELECTRO-OPTICAL DEVICE AND ELECTRONIC APPARATUS

PRELIMINARY AMENDMENT

Director of the U.S. Patent and Trademark Office
Washington, D. C. 20231

Sir:

Prior to initial examination, please amend the above-identified application as follows:

IN THE ABSTRACT:

Please replace the Abstract filed with the Substitute Abstract attached hereto.

IN THE SPECIFICATION:

Please replace the specification with the Substitute Specification attached hereto.

IN THE CLAIMS:

Please replace claims 1-9 as follows:

1. (Amended) An electro-optical device, comprising:

an electro-optical material;

a substrate that supports the electro-optical material;

a first wiring formed over a surface of the substrate;

an insulating layer that covers the first wiring, the insulating layer being

formed over the surface of the substrate; and

a second wiring formed over a first region of the insulating layer that includes a region overlapping a region in which the electro-optical material is formed, and a second region which corresponds to a remaining region other than the first region of the insulating layer, the second wiring being connected to the first wiring via a contact hole formed within the first region of the insulating layer.

2. (Amended) The electro-optical device according to claim 1, further including a counter substrate, the electro-optical material being sandwiched between the substrate and the counter substrate, a sealing material being disposed between the substrate and the counter substrate,

the first region including a region of the insulating layer which faces the sealing material.

3. (Amended) The electro-optical device according to claim 1, the insulating layer further including a mounting region overlaid with an electronic component mounted on the surface of the insulating layer, and the second wiring being connected to the first wiring via a contact hole formed in the mounting region of the insulating layer.

4. (Amended) An electro-optical device, comprising:
an electro-optical material;
a substrate that supports the electro-optical material;
a first wiring formed over a surface of the substrate;
an insulating layer that covers the first wiring, the insulating layer being formed over the surface of the substrate, and having a mounting region overlaid with an electronic component mounted over the surface of the insulating layer; and
a second wiring connected to the electronic component, the second wiring being formed over the surface of the insulating layer, and connected to the first wiring via a contact hole formed within the mounting region.

5. (Amended) The electro-optical device according to claim 3, the electronic component being an integrated circuit that includes an output terminal connected to the second wiring.

6. (Amended) The electro-optical device according to claim 3, the electronic component being a flexible substrate that includes a base having flexibility and a wiring formed on the surface of the base, the wiring being connected to the second wiring.

7. (Amended) The electro-optical device according to claim 1, the first wiring including at least one of an elemental metal and an alloy, and the second wiring including a conductive oxide.

8. (Amended) The electro-optical device according to claim 1, further including a pixel electrode that applies a voltage to the electro-optical material, the second wiring and the pixel electrode being formed of the same layer.

9. (Amended) An electronic apparatus, comprising:
the electro-optical device according to claim 1.

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REMARKS

Claims 1-9 are pending. By this Amendment, the Abstract and specification are replaced with a Substitute Abstract and Substitute Specification, and claims 1-9 are amended.

The attached Appendix includes marked-up copies of the specification (37 C.F.R. §1.125(b)(2)) and each rewritten claim (37 C.F.R. §1.121(c)(1)(ii)).

Prompt and favorable examination on the merits is respectfully requested.

Respectfully submitted,



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Attachments:

Substitute Abstract
Appendix
Substitute Specification
Marked-up Copy of Specification

Date: June 3, 2002

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ABSTRACT OF THE DISCLOSURE

In a liquid crystal display that includes a second substrate that supports a liquid crystal and data lines formed on the surface of the second substrate, a third insulating layer that covers the data lines is formed on the surface of the second substrate. On the surface of the third insulating layer, chip connecting wiring is formed over a covered region covered with a sealing material and the liquid crystal and the region other than the covered region. By connecting the chip connecting wiring to the data lines via contact holes formed within the covered region of the third insulating layer, corrosion of the wiring formed on the substrate is inhibited.

APPENDIX

Changes to Abstract:

The following is a marked-up version of the amended Abstract.

ABSTRACT OF THE DISCLOSURE

In a liquid crystal display ~~comprising that includes~~ a second substrate 20 ~~for that~~ support ~~carrying~~ a liquid crystal 40 and data lines 22 formed on the surface of the second substrate, a third insulating layer 246 ~~for that~~ covering the data lines 22 ~~are is~~ formed on the surface of the second substrate-20. On the surface of the third insulating layer-246, chip connecting wiring 25 ~~are is~~ formed over a covered region 61 ~~covered~~ with a sealing material 30 and the liquid crystal 40 and the region other than the covered region-61. By connecting the chip connecting wiring 25 to the data lines 22 via contact holes 25a formed within the covered region 61 of the third insulating layer-246, corrosion of the wiring formed on the substrate is inhibited.

Changes to Specification:

A Substitute Specification is attached in accordance with 37 C.F.R. 1.125(b)(2).

Changes to Claims:

The following are marked-up versions of the amended claims:

1. (Amended) An electro-optical device, comprising:
 - an electro-optical material;
 - a substrate ~~carrying an~~ that supports the electro-optical material;
 - a first wiring formed ~~on the~~ over a surface of the substrate;
 - an insulating layer ~~for that~~ covering the first wiring, the insulating layer being formed ~~on over~~ the surface of the substrate; and
 - a second wiring formed over a first region of the insulating layer that ~~includes~~ ing a region overlapping a region in which the electro-optical material is formed, and

a second region which corresponds to ~~the~~ a remaining region other than the first region of the insulating layer, the second wiring being connected to the first wiring via a contact hole formed within the first region of the insulating layer.

2. ~~(Amended) An~~ The electro-optical device according to claim 1, further ~~comprising~~ including a counter substrate, ~~with the electro-optical material~~ being sandwiched between the substrate and the counter substrate, ~~and a sealing material~~ being disposed between the substrate and the counter substrate,

~~wherein~~ the first region ~~including~~ includes a region of the insulating layer which faces the sealing material.

3. ~~(Amended) An~~ The electro-optical device according to ~~one of~~ claims 1 and 2, ~~wherein~~ the insulating layer further ~~comprises~~ including a mounting region overlaid with an electronic component mounted on the surface of the insulating layer, and the second wiring is being connected to the first wiring via a contact hole formed in the mounting region of the insulating layer.

4. ~~(Amended) An~~ The electro-optical device, comprising:

- an electro-optical material;
- ~~a substrate carrying an~~ that supports the electro-optical material;
- a first wiring formed ~~on the~~ over a surface of the substrate;
- an insulating layer ~~for that~~ covering the first wiring, the insulating layer being formed ~~on over~~ on the surface of the substrate, and having a mounting region overlaid with an electronic component mounted ~~on over~~ on the surface of the insulating layer; and
- a second wiring connected to the electronic component, the second wiring being formed ~~on over~~ on the surface of the insulating layer, and connected to the first wiring via a contact hole formed within the mounting region.

5. (Amended) An ~~The~~ electro-optical device according to claim 3 ~~or 4, wherein~~ the electronic component is being an integrated circuit ~~comprising that includes~~ an output terminal connected to the second wiring.

6. (Amended) An ~~The~~ electro-optical device according to claim 3 ~~or 4, wherein~~ the electronic component is being a flexible substrate ~~comprising that includes~~ a base having flexibility and a wiring formed on the surface of the base, the wiring being connected to the second wiring.

7. (Amended) An ~~The~~ electro-optical device according to ~~one of~~ claims 1 ~~to 6,~~ wherein the first wiring ~~comprises including~~ at least one of an elemental metal or and an alloy, and the second wiring ~~comprises including~~ a conductive oxide.

8. (Amended) An ~~The~~ electro-optical device according to ~~one of~~ claims 1 ~~to 7,~~ further ~~comprising including~~ a pixel electrode ~~for that applies~~ a voltage to the electro-optical material, wherein the second wiring and the pixel electrode ~~are being~~ formed of the same layer.

9. (Amended) An electronic apparatus, comprising:
_____ ~~an the~~ electro-optical device according to ~~one of~~ claims 1 ~~to 8.~~

ELECTRO-OPTICAL DEVICE AND ELECTRONIC APPARATUS

BACKGROUND OF THE INVENTION

1. Field of Invention

[0001] The present invention relates to an electro-optical device and an electronic apparatus which use an electro-optical material, such as liquid crystal and an electroluminescent (EL) material.

2. Description of Related Art

[0002] Electro-optical devices are widely used as display units for electronic apparatuses. Typical examples of such display units are liquid crystal displays and electroluminescent (EL) displays. Such an electro-optical device generally includes a substrate that supports an electro-optical material, such as liquid crystal and an electroluminescent element, and electrodes that apply a voltage to the electro-optical material. In accordance with this type of electro-optical device, a structure has been suggested in which wiring that supplies signals to the electrodes is provided, and extends toward the edges of the substrate while being connected to an IC chip, which is mounted on the substrate by the chip-on-glass (COG) method.

SUMMARY OF THE INVENTION

[0003] The above-described structure, however, is subject to a problem in that the wiring that extends toward the edges of the substrate is exposed to outside air, and thus the exposed portions easily suffer from corrosion caused by deposition of water or the like. From the viewpoint of wiring resistance and the like, the wiring is preferably formed of a metal, such as aluminum. However, since such a metal is vulnerable to corrosion, the above problem is more noticeable.

[0004] The present invention address the above circumstances, and provides an electro-optical device and an electronic apparatus which suffer less from corrosion of the wiring formed on the substrate.

[0005] To address this problem, an electro-optical device of the present invention includes: a substrate that supports an electro-optical material; a first wiring formed on the surface of the substrate; an insulating layer that covers the first wiring, the insulating layer being formed on the surface of the substrate; and a second wiring formed over a first region of the insulating layer including a region overlapping a region in which the electro-optical material is formed, and a second region which corresponds to the remaining region other than

the first region of the insulating layer, the second wiring being connected to the first wiring via a contact hole formed within the first region of the insulating layer.

[0006] In accordance with this structure, since the first wiring is covered with the insulating layer, corrosion of the first wiring due to deposition of moisture or the like can be reduced or prevented. Moreover, since a contact hole that connects the first wiring to the second wiring is formed in the first region including the region overlapping the region in which the electro-optical material is formed, moisture can be reduced or prevented from reaching the vicinity of the contact hole. Accordingly, where, for example, the second wiring is made of a material which readily permeates moisture, the moisture does not permeate through the second wiring to reach the first wiring. Thus, corrosion of the first wiring can be reliably reduced or prevented.

[0007] Where the above electro-optical device is applied to a liquid crystal display that uses liquid crystal as the electro-optical material, the electro-optical device includes a counter substrate that holds the electro-optical material between the substrate and the counter substrate, and a sealing material disposed between the substrate and the counter substrate. In such a case, the first region preferably includes a region of the insulating layer facing the sealing material. That is, where the contact hole is formed in the region of the insulating layer overlaid with the sealing material, the amount of moisture can be reduced, or moisture can be prevented, from reaching the vicinity of the contact holes, and corrosion of the first wiring can be reliably suppressed.

[0008] Furthermore, in order to address the above problem, an electro-optical device of the present invention includes: a substrate that supports an electro-optical material; a first wiring formed on the surface of the substrate; an insulating layer that covers the first wiring, the insulating layer being formed on the surface of the substrate and having a mounting region overlaid with an electronic component mounted on the surface of the insulating layer; and a second wiring connected to the electronic component, the second wiring being formed on the surface of the insulating layer and connected to the first wiring via a contact hole formed within the mounting region.

[0009] According to this electro-optical device, as in the above, the first wiring is covered with the insulating layer to reduce or prevent corrosion of the first wiring. In addition, since contact holes that connect the first wiring to the second wiring are provided in the region to mount the electronic component, the amount of moisture can be reduced, or moisture can be prevented, from reaching the vicinity of the contact hole. Accordingly, where, for example, the second wiring is made of material which readily permeates moisture,

the amount of moisture can be reduced, or the moisture can be reliably prevented, from reaching the first wiring, thereby securely preventing corrosion of the first wiring. An example of the electro-optical device is an integrated circuit having an output terminal connected to the second wiring. An example of the electronic component is a flexible substrate that includes a base having flexibility and a wiring formed on the surface of the base, the wiring being connected to the second wiring.

[0010] In the electro-optical device of the present invention, the first wiring is preferably made of an elemental metal or an alloy, whereas the second wiring is preferably made of a conductive oxide. In this manner, the second wiring has a higher corrosion resistance than the first wiring. Hence, the problem of wiring corrosion can be addressed or overcome even where a structure is employed that includes a second wiring that is exposed to the outside.

[0011] In an electro-optical device that further includes a pixel electrode that applies a voltage to the electro-optical material, the second wiring and the pixel electrode are preferably formed of the same layer. In this manner, the pixel electrode and the second wiring can be made in the same step, thereby reducing manufacturing costs. In this case, both the pixel electrode and the second wiring may be made of indium tin oxide (ITO).

[0012] To address the above problem, an electronic apparatus of the present invention includes the electro-optical device described above. As is described above, in the electro-optical device of the present invention, corrosion of the wiring formed on the substrate can be reduced or prevented. Thus, the electronic apparatus using this electro-optical device is free or substantially free from degradation of the display quality that is caused by the corrosion of the wiring of the electro-optical device.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] Fig. 1 is a plan view showing the structure of a liquid crystal display according to a first embodiment of the present invention;

[0014] Fig. 2 is a cross-sectional view taken along plane A-A' in Fig. 1;

[0015] Fig. 3 is a plan view showing the structure of a portion above the second substrate of the liquid crystal display;

[0016] Fig. 4 is a cross-sectional view showing the structure of the vicinity of a TFT in the liquid crystal display;

[0017] Fig. 5(a) is a plan view showing the structure of the vicinity of a projection in the liquid crystal display, and Fig. 5(b) is a cross-sectional view taken along plane B-B' in Fig. 5(a);

[0018] Fig. 6(a) is a plan view showing the structure of a comparative example for the liquid crystal display, and Fig. 6(b) is a cross-sectional view taken along plane C-C' in (a);

[0019] Fig. 7(a) is a plan view showing the structure of the vicinity of a projection in a liquid crystal display according to a second embodiment of the present invention, and Fig. 7(b) is a cross-sectional view taken along plane D-D' in (a);

[0020] Fig. 8(a) is a perspective view showing the structure of a personal computer, which is an example of an electronic apparatus incorporating the electro-optical device of the present invention, and Fig. 8(b) is a perspective view showing the structure of a cellular phone, which is another example of an electronic apparatus incorporating the electro-optical device of the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0021] The preferred embodiments of the present invention will now be described with reference to the drawings. Each of the embodiments below represents an aspect of the present invention and by no means limits the scope of the invention. Various modifications are possible without departing from the technical scope of the present invention. In each of the drawings referenced below, layers and components are illustrated at different scales so as to make each of the layers and components sufficiently identifiable.

<A: First Embodiment>

[0022] An active matrix reflective liquid crystal display according to a first embodiment of the present invention will now be described. In this embodiment, a thin film transistor (TFT), which is a three-terminal switching element, is illustrated as a switching element.

[0023] Fig. 1 is a plan view showing the structure of a liquid crystal display according to this embodiment. Fig. 2 is a cross-sectional view taken along plane A-A' in Fig. 1. As illustrated in Figs. 1 and 2, a liquid crystal display 1 includes: a first substrate 10 and a second substrate 20 facing the first substrate 10, which are bonded to each other through a sealing material 30 therebetween; and a liquid crystal 40, i.e., the electro-optical material, enclosed between the two substrates. The sealing material 30 is shaped into a frame having a substantially rectangular form extending along each side of the first substrate 10. The liquid crystal 40 is enclosed in the inner region surrounded by the sealing material 30.

[0024] Each of the first substrate 10 and the second substrate 20 is a plate having light transmitting property, such as a glass plate, a quartz plate, a plastic plate, or the like. Counter electrodes 11 made of a transparent conductive material, such as indium tin oxide (ITO), are formed over the entire inner surface of the first substrate 10 (the side facing the

liquid crystal 40). The inner surfaces of the first and second substrates 10 and 20 are covered with alignment layers which are subjected to a rubbing treatment in predetermined directions. A polarizer that polarizes incident light, a retardation film that compensates for interference colors, and the like are bonded on the outer surface of the first substrate 10; however, the description and illustration of these components are omitted since they are not directly related to the present invention.

[0025] As shown in Fig. 1, the second substrate 20 includes a section 201 projecting from the outer edges of the sealing material 30 in the negative X axis direction and the positive Y axis direction. This section is hereinafter referred to as "the projection 201." In this embodiment, as shown in Figs. 1 and 2, the edges of the first substrate 10 and the outer edges of the sealing material 30 are aligned and overlaid with each other when viewed in a direction perpendicular to the surface of the first substrate 10. Thus, the projection 201 can also be regarded as a region of the second substrate 20 projecting from the first substrate 10. A Y driver IC 51 and an X driver IC 52 are mounted on the projection 201 by the COG method. The Y driver IC 51 and the X driver IC 52 have circuits that output drive signals, i.e., a scanning signal and a data signal, that drive the liquid crystal 40.

[0026] As shown in Fig. 3, a plurality of scanning lines 21 extending in the X direction, a plurality of data lines 22 extending in the Y direction, pixel electrodes 23 disposed to correspond to the intersections of the scanning lines 21 and the data lines 22, and thin film transistors (TFTs) 24 are provided on the inner surface of the second substrate 20 (the side facing the liquid crystal 40). Each of the scanning lines 21 crosses one side of the sealing material 30, i.e., the side at the left in Fig. 1, reaches the projection 201, and is connected to the output terminal of the Y driver IC 51 at the end thereof. A scanning signal output from the Y driver IC 51 is thus supplied to each of the scanning lines 21. The pixel electrodes 23 have a substantially rectangular shape and are formed of a transparent conductive material, such as ITO. The pixel electrodes 23 are aligned in a matrix on the surface of the second substrate 20 so as to face the counter electrodes 11 on the first substrate 10. The pixel electrodes 23 are connected to the scanning lines 21 and the data lines 22 through TFTs 24, which are three-terminal switching elements. The details are described below.

[0027] Fig. 4 is a cross-sectional view showing the structures of the TFT 24 and the vicinity of the TFT 24. As shown in Fig. 4, a first insulating layer 241, formed of SiO₂ or the like, is formed on the inner surface of the second substrate 20 as the base layer to form the TFT 24. A semiconductor layer 242, coated with an insulating film 243, is disposed on the

surface of the first insulating layer 241. A region of the semiconductor layer 242 overlaid with the scanning line 21 is defined as a channel region 242a. In other words, as shown in Fig. 3, each scanning line 21 includes a portion extending in the X direction and portions branching from this portion which intersect with the semiconductor layers 242. Each intersection functions as a gate electrode 211 shown in Fig. 4. The surface of the first insulating layer 241 including the semiconductor layer 242 and the gate electrode 211 is covered with a second insulating layer 245 formed of SiO₂ or the like.

[0028] As shown in Fig. 4, the semiconductor layer 242 includes a lightly doped source region 242b and a heavily doped source region 242c disposed at the source-side of the channel region 242a, and a lightly doped drain region 242d and a heavily doped drain region 242e disposed at the drain-side of the channel region 242a to form what is known as a "lightly-doped-drain" (LDD) structure. The heavily doped source region 242c is connected to the data line 22, which extends in a direction perpendicular to the plane of the paper of Fig. 4, via a contact hole 22a penetrating the insulating film 243 on the surface of the semiconductor layer 242 and the second insulating layer 245. In this embodiment, the data line 22 is formed of aluminum. The heavily doped drain region 242e is connected to an intermediate conductive film 244, formed of the same layer as the data line 22, via a contact hole 244a penetrating the insulating film 243 on the surface of the semiconductor layer 242 and the second insulating layer 245.

[0029] A third insulating layer 246 covers the surface of the second insulating layer 245 having the data line 22 and the intermediate conductive film 244. The third insulating layer 246 includes a thin film 246a formed of SiN or the like and a resin layer 246b formed of a resin material, for example, acrylic or epoxy resin. The pixel electrode 23 is formed on the surface of the third insulating layer 246 and is connected to the intermediate conductive film 244 via a contact hole 23a in the third insulating layer 246. In other words, the pixel electrode 23 is connected to the heavily doped drain region 242e of the semiconductor layer 242 via the intermediate conductive film 244.

[0030] As shown in Fig. 4, the surface of the third insulating layer 246 on which the pixel electrode 23 is formed is rough and includes numerous microscopic corrugations. A reflector 247 formed of a light-reflective metal, such as aluminum or silver, is disposed between this rough surface and the pixel electrode 23. Since the reflector 247 is formed as a thin film on the corrugated surface of the third insulating layer 246, the surface of the reflector 247 also has corrugations corresponding to the corrugations on the surface of the third insulating layer 246. As a result, the incident light from the first substrate 10 is

adequately scattered by the corrugations formed on the surface of the reflector 247 and is emitted from the first substrate 10 side. Accordingly, the images visually observed by an observer are free of background reflection and reflection of light from the room lighting.

[0031] Next, referring to Figs. 5(a) and 5(b), the structure of the data line 22 and the structure of the vicinity of the projection 201 are explained. As shown in Figs. 5(a) and 5(b), the data line 22 extends from the region surrounded by the sealing material 30, crossing one side of the sealing material 30, i.e., the side at the bottom in Fig. 1, and reaches the projection 201. The data line 22 leading to the projection 201 is covered with the third insulating layer 246, as shown in Fig. 5(b). In other words, while each data line 22 in the inner region of the sealing material 30, i.e., the vicinity of the TFT 24 to be more specific, is coated with the third insulating layer 246, as shown in Fig. 4, each data line 22 in this embodiment is covered with the third insulating layer 246 over the entire length, including the portion extending onto the projection 201. Note that in Figs. 5(a) and 5(b), the first insulating layer 241 and the second insulating layer 245 shown in Fig. 4 are omitted from the drawing in order to avoid unnecessary complexity. However, the data line 22 is actually disposed on the surface of these insulating layers covering the second substrate 20.

[0032] A chip connecting wiring 25 is formed on the surface of the third insulating layer 246 corresponding to each data line 22. The chip connecting wiring 25 extends from the region surrounded by the sealing material 30 to outside the sealing material 30, i.e., the projection 201. In detail, the chip connecting wiring 25 is formed on the surface of the third insulating layer 246 so as to face the portion of the data line 22 extending from the vicinity of the interior of the sealing material 30 to the projection 201, and serves to connect the data line 22 to the X driver IC 52. In this embodiment, the chip connecting wiring 25 is formed during a step for forming pixel electrodes 23. This step includes patterning of the ITO layer covering the third insulating layer 246. Accordingly, the chip connecting wiring 25 is formed of ITO as is the pixel electrode 23.

[0033] As shown in Figs. 5(a) and 5(b), each chip connecting wiring 25 is connected to the corresponding data line 22 via contact holes 25a formed in the third insulating layer 246. Moreover, the contact holes 25a of this embodiment are formed in the region surrounded by the outer edges of the sealing material 30, as shown in Figs. 5(a) and 5(b). In detail, the contact holes 25a are formed in a region comprising the portion of the third insulating layer 246 facing or contacting the sealing material 30 and the portion inside the sealing material 30 facing the liquid crystal 40. This region is hereinafter referred to as the "covered region 61".

[0034] Where the X driver IC 52 is mounted on the projection 201, i.e., on the third insulating layer 246 to be more precise, via an anisotropic conductive film, the conductive particles in the anisotropic conductive film connect the chip connecting wiring 25 to an output terminal 52a of the X driver IC 52. As a result, each output terminal 52a of the X driver IC 52 is electrically connected to the corresponding data line 22 via the chip connecting wiring 25, and a data signal output from the X driver IC 52 is supplied to the data line 22.

[0035] As is described above, according to this embodiment, the data lines 22 on the projection 201 are covered with the third insulating layer 246. In other words, the portions of the data lines 22 on the projection 201 as well as those surrounded by the sealing material 30 do not come into contact with the outside air. Hence, corrosion of the data lines 22 caused by moisture in the outside air can be effectively reduced or prevented. Moreover, in this embodiment, each data line 22 is connected to the corresponding output terminal 52a of the X driver IC 52 via the chip connecting wiring 25. Accordingly, the entire length of the data line 22 including the portion connected to the output terminal 52a is reduced or prevented from being exposed to the outside, and corrosion of the data lines 22 can be securely reduced or prevented.

[0036] Each chip connecting wiring 25 of this embodiment is formed in the same step as the pixel electrodes 23. Accordingly, the manufacturing cost is lower compared to the case in which the chip connecting wiring 25 and the pixel electrodes 23 are manufactured in separate steps. Furthermore, since ITO is a chemically stable oxide and is resistant to corrosion, the problem of corrosion does not occur, even if a structure is employed that includes chip connecting wiring 25 exposed to the outside air, as shown in Figs. 5(a) and 5(b).

[0037] Moreover, in this embodiment, the contact holes 25a that connect the chip connecting wiring 25 to the data line 22 are formed in the covered region 61 to securely prevent corrosion of the data lines 22. The details are described below.

[0038] Figs. 6(a) and 6(b) show a comparative example in which the contact holes 25a are formed in the region other than the covered region 61, i.e., the region outside of the sealing material 30. In this structure, since the major parts of the data line 22 are covered with the third insulating layer 246 excluding the portion at the contact holes 25a, the corrosion of the data lines 22 is less significant compared to the case where no third insulating layer 246 is provided and data lines 22 are exposed at the projection 201. However, ITO constituting the chip connecting wiring 25 readily allows moisture to

reducing or preventing moisture in the outside air from reaching the vicinity of the contact holes 25a.

[0048] (2) Each of the above-described embodiments illustrates a structure having the X driver IC 52 which is mounted on the projection 201 by the COG method. Alternatively, a flexible substrate having an X driver IC mounted thereon may be mounted on the projection 201. In particular, a flexible substrate including a film base having an X driver IC mounted thereon and wiring formed on the surface of the film base is bonded to the projection 201 through an anisotropic conductive layer. The conductive particles in the anisotropic conductive layer then electrically connect the wiring of the flexible substrate to the chip connecting wiring 25 on the projection 201. Where the flexible substrate is mounted instead of the X driver IC 52 in the structure illustrated in the second embodiment, the region of the third insulating layer 246 bonded to the flexible substrate (more specifically, the region covered with the film base) is defined as the "mounting region". Accordingly, the contact holes 25a formed in this mounting region to connect the chip connecting wiring 25 to the data line 22 prevents moisture in the outside air from reaching the vicinity of the contact holes 25a due to the film base, achieving the same advantages as in the above-described second embodiment. In view of the above, the "electronic component" in these embodiments is not limited to a driver IC, and instead includes various components having electrodes or wiring to be connected to the chip connecting wiring 25.

[0049] (3) In the first embodiment, each data line 22 extends over the projection 201. However, since the data line 22 and the chip connecting wiring 25 are connected to each other at the covered region 61, the data lines 22 need not extend over the projection 201. Moreover, although only the data lines 22 are connected to the output terminals 52a of the Y driver IC 52 through the chip connecting wiring 25 in the above-described embodiments and modifications, the same arrangement may naturally be employed for the scanning lines 21.

[0050] (4) In each of the above-described embodiments, the data lines 22 are made of aluminum, and the chip connecting wiring 25 are made of ITO. However, this does not limit the material of this wiring. From the point of view of preventing corrosion of the data lines 22, the material for the chip connecting wiring 25 is preferably chemically more stable, i.e., more corrosion-resistant, than that for the data lines 22. For example, while the data lines 22 may be made of an elemental metal, such as silver, copper, or chromium, or of an alloy containing these metals, the chip connecting wiring 25 may be made of SnO₂, gold, platinum, or the like, which is chemically more stable than the data lines 22. Although the

SPECIFICATION

ELECTRO-OPTICAL DEVICE AND ELECTRONIC APPARATUS

BACKGROUND OF THE INVENTIONTECHNICAL FIELD OF THE INVENTION1. Field of the Invention

[0001] The present invention relates to an electro-optical device and an electronic apparatus which use an electro-optical material such as liquid crystal and an electroluminescent (EL) material.

2. Description of the Related Art
DESCRIPTION OF THE RELATED ART

[0002] As is generally known, electro-optical devices are widely used as display units for electronic apparatuses. Typical examples of such display units are liquid crystal displays and electroluminescent (EL) displays. Such an electro-optical device generally ~~comprises~~ ^{includes} a substrate ~~for carrying~~ ^{that supports} an electro-optical material, such as liquid crystal and an electroluminescent element, and electrodes ~~for applying~~ ^{that} a voltage to the electro-optical material. For this type of electro-optical device, a structure has been suggested in which wiring ~~for supplying~~ ^{in accordance with that is} signals to the electrodes is provided and ~~extended~~ ^{delivered} toward the edges of the substrate while being connected to an IC chip which is mounted on the substrate by the chip-on-glass (COG) method.

SUMMARY OF THE INVENTION

[0003] The above-described structure, however, ~~has~~ ^{is subject to} a problem in that the wiring ~~extending~~ ^{is} toward the edges of the substrate ~~are~~ ^{is} exposed to outside air and thus the exposed portions easily suffer from corrosion caused by deposition of water or the like. From the viewpoint of wiring resistance and the like, the wiring ~~are~~ ^{is} preferably ~~composed~~ ^{formed} of a metal, such as aluminum; however, since such a metal is vulnerable to corrosion, the above problem is more noticeable.

[0004] The present invention ~~has been made in view of~~ ^{addresses} the above circumstances, and ~~aims to~~ ^{aims to} provide an electro-optical device and an electronic apparatus which suffer less from corrosion of the wiring formed on the substrate.

[0005] To ~~overcome~~ ^{address} this problem, an electro-optical device of the present invention ~~comprises~~ ^{includes} a substrate ~~carrying~~ ^{that supports} an electro-optical material; a first wiring formed on the surface of the substrate; an insulating layer ~~for covering~~ ^{that} the first wiring, the insulating layer

being formed on the surface of the substrate; and a second wiring formed over a first region of the insulating layer including a region overlapping a region in which the electro-optical material is formed and a second region which corresponds to the remaining region other than the first region of the insulating layer, the second wiring being connected to the first wiring via a contact hole formed within the first region of the insulating layer.

[0006] ^{for accordance} With this structure, since the first wiring is covered with the insulating layer, corrosion of the first wiring due to deposition of moisture or the like ^{reduced or} can be prevented. Moreover, since a contact hole ^{that} ^s for connecting the first wiring to the second wiring is formed in the first region including the region overlapping the region in which the electro-optical material is formed, moisture can be ^{reduced or} prevented from reaching the vicinity of the contact hole. Accordingly, ^{where} when, for example, the second wiring is made of a material which readily permeates moisture, the moisture does not permeate through the second wiring ^{to} and reach the first wiring. Thus, corrosion of the first wiring can be reliably ^{reduced or} prevented.

[0007] ^{where} When the above electro-optical device is applied to a liquid crystal display ^{that} using liquid crystal as the electro-optical material, the electro-optical device includes a counter substrate ^{that} ^s for holding the electro-optical material between the substrate and the counter substrate, and a sealing material disposed between the substrate and the counter substrate. In such a case, the first region preferably includes a region of the insulating layer facing the sealing material. That is, ^{where} when the contact hole is formed in the region of the insulating layer overlaid with the sealing material, ^{the amount of moisture can be reduced,} moisture can be prevented from reaching the vicinity of the contact holes, and corrosion of the first wiring can be reliably suppressed.

[0008] Furthermore, in order to ^{address} overcome the above problem, an electro-optical device of the present invention ^{includes} comprises: a substrate ^{that supports} carrying an electro-optical material; a first wiring formed on the surface of the substrate; an insulating layer ^{that} ^s for covering the first wiring, the insulating layer being formed on the surface of the substrate and having a mounting region overlaid with an electronic component mounted on the surface of the insulating layer; and a second wiring connected to the electronic component, the second wiring being formed on the surface of the insulating layer and connected to the first wiring via a contact hole formed within the mounting region.

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[0009] According to this electro-optical device, as in the above, the first wiring is covered with the insulating layer to ^{reduce or} prevent corrosion of the first wiring. In addition, since contact holes for connecting the first wiring to the second wiring are provided in the region ^{that} for mounting the electronic component, ^{the amount of moisture can be reduced, or} moisture can be prevented from reaching the vicinity of the contact hole. Accordingly, ^{where} when, for example, the second wiring is made of material ^{the amount of moisture can be reduced, or} which readily permeates moisture, the moisture can be reliably prevented from reaching the first wiring, thereby securely preventing corrosion of the first wiring. An example of the electro-optical device is an integrated circuit having an output terminal connected to the second wiring. An example of the electronic component is a flexible substrate ^{that includes} comprising a base having flexibility and a wiring formed on the surface of the base, the wiring being connected to the second wiring.

[0010] In the electro-optical device of the present invention, the first wiring is preferably made of an elemental metal or an alloy, whereas the second wiring is preferably made of a conductive oxide. In this manner, the second wiring has a higher corrosion resistance than the first wiring, ^{hence,} the problem of wiring corrosion can be overcome even ^{where} when a structure ^{is employed that includes} including a second wiring, ^{that is, to the} exposed outside is employed ^{that further includes}.

[0011] In an electro-optical device ^{is} further comprising a pixel electrode for ^{that} applying a voltage to the electro-optical material, the second wiring and the pixel electrode are preferably formed of the same layer. In this manner, the pixel electrode and the second wiring can be made in the same step, thereby reducing ^{the} manufacturing cost. ^{Note that in} this case, both the pixel electrode and the second wiring may be made of indium tin oxide (ITO).

[0012] To ^{address} overcome the above problem, an electronic apparatus of the present invention ^{includes} comprises the electro-optical device described above. As is described above, in the electro-optical device of the present invention, corrosion of the wiring formed on the substrate can be ^{reduced or} prevented. Thus, the electronic apparatus using this electro-optical device is ^{or substantially free} free from degradation of the display quality ^{that is} caused by the corrosion of the wiring of the electro-optical device.

BRIEF DESCRIPTION OF THE DRAWINGS ^{center}

[0013] Fig. 1 is a plan view showing the structure of a liquid crystal display according to a first embodiment of the present invention.

Fig. 2 is a cross-sectional view taken along line A-A' in Fig. 1;

Fig. 3 is a plan view showing the structure of a portion above the second substrate of the liquid crystal display;

Fig. 4 is a cross-sectional view showing the structure of the vicinity of a TFT in the liquid crystal display;

Fig. 5(a) is a plan view showing the structure of the vicinity of a projection in the liquid crystal display, and (b) is a cross-sectional view taken along line B-B' in Fig. 5(a);

Fig. 6(a) is a plan view showing the structure of a comparative example for the liquid crystal display, and (b) is a cross-sectional view taken along line C-C' in (a);

Fig. 7(a) is a plan view showing the structure of the vicinity of a projection in a liquid crystal display according to a second embodiment of the present invention, and (b) is a cross-sectional view taken along line D-D' in (a);

Fig. 8(a) is a perspective view showing the structure of a personal computer which is an example of an electronic apparatus incorporating the electro-optical device of the present invention, and (b) is a perspective view showing the structure of a cellular phone which is another example of an electronic apparatus incorporating the electro-optical device of the present invention.

~~DESCRIPTION OF REFERENCE NUMERALS~~

[0014] 1: liquid crystal display (electro-optical device), 10: first substrate (counter substrate), 11: counter electrode, 20: second substrate (substrate), 201: projection (second region), 21: scanning line, 22: data line (first wiring), 23: pixel electrode, 24: TFT, 246: third insulating layer (insulating layer), 25: chip connecting wiring 25 (second wiring), 25a: contact hole, 30: sealing material, 40: liquid crystal (electro-optical material), 51: X driver IC (electronic component), 52: Y driver IC, 61: covered region (first region), 62: mounting region, 81: personal computer (electronic apparatus), and 82: cellular phone (electronic apparatus).

~~BEST MODE FOR CARRYING OUT THE INVENTION~~

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0015] The preferred embodiments of the present invention will now be described with reference to the drawings. Each of the embodiments below represents an aspect of the present invention and by no means limits the scope of the invention. Various modifications are possible without departing from the technical scope of the present invention. In each of the drawings referenced below, layers and components are illustrated at different scales so as to make each of the layers and components sufficiently identifiable.

<A: First Embodiment>

[0016] An active matrix reflective liquid crystal display according to a first embodiment of the present invention will now be described. In this embodiment, a thin film transistor (TFT), which is a three-terminal switching element, is illustrated as a switching element.

[0017] Fig. 1 is a plan view showing the structure of a liquid crystal display according to this embodiment. Fig. 2 is a cross-sectional view taken along line A-A' in Fig. 1. As illustrated in ~~these drawings~~ ^{Figs. 1 and 2}, a liquid crystal display 1 ~~comprises~~ ^{includes} a first substrate 10 and a second substrate 20 facing the first substrate 10, which are bonded to each other through a sealing material 30 therebetween; and a liquid crystal 40, i.e., the electro-optical material, enclosed between the two substrates. The sealing material 30 is shaped into a frame having a substantially rectangular form extending along each side of the first substrate 10. The liquid crystal 40 is enclosed in the inner region surrounded by the sealing material 30.

[0018] Each of the first substrate 10 and the second substrate 20 is a plate having light transmitting property, such as a glass plate, a quartz plate, a plastic plate, or the like. Counter electrodes 11 made of a transparent conductive material, such as indium tin oxide (ITO) are formed over the entire inner surface of the first substrate 10 (the side facing the liquid crystal 40). The inner surfaces of the first and second substrates 10 and 20 are covered with alignment layers which are subjected to a rubbing treatment in predetermined directions. A polarizer ~~for polarizing~~ ^{that} incident light, a retardation film ~~for compensating~~ ^{that} for interference colors, and the like are bonded on the outer surface of the first substrate 10; however, the description and illustration of these components are omitted since they are not directly related to the present invention.

[0019] As shown in Fig. 1, the second substrate 20 includes a section 201 projecting from the outer edges of the sealing material 30 in the negative X axis direction and the positive Y axis direction. This section is hereinafter referred to as the projection 201. In this embodiment, as shown in Figs. 1 and 2, the edges of the first substrate 10 and the outer edges of the sealing material 30 are aligned and overlaid with each other when viewed in a direction perpendicular to the surface of the first substrate 10. Thus, the projection 201 can also be regarded as a region of the second substrate 20 projecting from the first substrate 10. A Y driver IC 51 and an X driver IC 52 are mounted on the projection 201 by the COG method. The Y driver IC 51 and the X driver IC 52 have circuits that output drive signals, i.e., a scanning signal and a data signal, for driving the liquid crystal 40.

[0020] As shown in Fig. 3, a plurality of scanning lines 21 extending in the X direction, a plurality of data lines 22 extending in the Y direction, pixel electrodes 23 disposed to correspond to the intersections of the scanning lines 21 and the data lines 22, and thin film transistors (TFTs) 24 are provided on the inner surface of the second substrate 20 (the side facing the liquid crystal 40). Each of the scanning lines 21 crosses one side of the sealing material 30, i.e., the side at the left in Fig. 1, reaches the projection 201, and is connected to the output terminal of the Y driver IC 51 at the end thereof. A scanning signal output from the Y driver IC 51 is thus supplied to each of the scanning lines 21. The pixel electrodes 23 have a substantially rectangular shape and are formed of a transparent conductive material such as ITO. The pixel electrodes 23 are aligned in a matrix on the surface of the second substrate 20 so as to face the counter electrodes 11 on the first substrate 10. The pixel electrodes 23 are connected to the scanning lines 21 and the data lines 22 through TFTs 24, which are three-terminal switching elements. The details are described below.

[0021] Fig. 4 is a cross-sectional view showing the structures of the TFT 24 and the vicinity of the TFT 24. As shown in the drawing, a first insulating layer 241 composed of SiO₂ or the like is formed on the inner surface of the second substrate 20 as the base layer for forming the TFT 24. A semiconductor layer 242 coated with an insulating film 243 is disposed on the surface of the first insulating layer 241. A region of the semiconductor layer

242 overlaid with the scanning line 21 is defined as a channel region 242a. In other words, as shown in Fig. 3, each scanning line 21 includes a portion extending in the X direction and portions branching from this portion which intersect with the semiconductor layers 242. Each intersection functions as a gate electrode 211 shown in Fig. 4. The surface of the first insulating layer 241 including the semiconductor layer 242 and the gate electrode 211 is covered with a second insulating layer 245 ^{formed} composed of SiO₂ or the like.

[0022] As shown in Fig. 4, the semiconductor layer 242 includes a lightly doped source region 242b and a heavily doped source region 242c disposed at the source-side of the channel region 242a, and a lightly doped drain region 242d and a heavily doped drain region 242e disposed at the drain-side of the channel region 242a to form what is known as a lightly-doped-drain (LDD) structure. The heavily doped source region 242c is connected to the data line 22, which extends in a direction perpendicular to the plane of the paper of the drawing, ^{Fig. 4} via a contact hole 22a penetrating the insulating film 243 on the surface of the semiconductor layer 242 and the second insulating layer 245. In this embodiment, the data line 22 is ^{formed} composed of aluminum. The heavily doped drain region 242e is connected to an intermediate conductive film 244, formed of the same layer as the data line 22, via a contact hole 244a penetrating the insulating film 243 on the surface of the semiconductor layer 242 and the second insulating layer 245.

[0023] A third insulating layer 246 covers the surface of the second insulating layer 245 having the data line 22 and the intermediate conductive film 244. The third insulating layer 246 ^{includes} comprises a thin film 246a ^{formed} composed of SiN or the like and a resin layer 246b ^{formed} composed of a resin material, for example, acrylic or epoxy resin. The pixel electrode 23 is formed on the surface of the third insulating layer 246 and is connected to the intermediate conductive film 244 via a contact hole 23a in the third insulating layer 246. In other words, the pixel electrode 23 is connected to the heavily doped drain region 242e of the semiconductor layer 242 via the intermediate conductive film 244.

[0024] As shown in Fig. 4, the surface of the third insulating layer 246 on which the pixel electrode 23 is formed is rough and includes numerous microscopic corrugations. A reflector ^{formed} 247 ³ composed of a light-reflective metal such as aluminum or silver is disposed ³

between this rough surface and the pixel electrode 23. Since the reflector 247 is formed as a thin film on the corrugated surface of the third insulating layer 246, the surface of the reflector 247 also has corrugations corresponding to the corrugations on the surface of the third insulating layer 246. As a result, the incident light from the first substrate 10 is adequately scattered by the corrugations formed on the surface of the reflector 247 and is emitted from the first substrate 10 side. Accordingly, the images visually observed by an observer are free of background reflection and reflection of light from the room lighting.

[0025] Next, referring to Fig. 5(a) and (b), the structure of the data line 22 and the structure of the vicinity of the projection 201 are explained. As shown in these drawings, the data line 22 extends from the region surrounded by the sealing material 30, crossing one side of the sealing material 30, i.e., the side at the bottom in Fig. 1, and reaches the projection 201. The data line 22 leading to the projection 201 is covered with the third insulating layer 246, as shown in Fig. 5(b). In other words, while each data line 22 in the inner region of the sealing material 30, i.e., the vicinity of the TFT 24 to be more specific, is coated with the third insulating layer 246, as shown in Fig. 4, each data line 22 in this embodiment is covered with the third insulating layer 246 over the entire length, including the portion extending onto the projection 201. Note that in Fig. 5(a) and (b), the first insulating layer 241 and the second insulating layer 245 shown in Fig. 4 are omitted from the drawing in order to avoid unnecessary complexity. However, the data line 22 is actually disposed on the surface of these insulating layers covering the second substrate 20.

[0026] A chip connecting wiring 25 is formed on the surface of the third insulating layer 246 corresponding to each data line 22. The chip connecting wiring 25 extends from the region surrounded by the sealing material 30 to outside the sealing material 30, i.e., the projection 201. In detail, the chip connecting wiring 25 is formed on the surface of the third insulating layer 246 so as to face the portion of the data line 22 extending from the vicinity of the interior of the sealing material 30 to the projection 201, and serves to connect the data line 22 to the X driver IC 52. In this embodiment, the chip connecting wiring 25 is formed during a step for forming pixel electrodes 23. This step comprises patterning of the ITO layer covering the third insulating layer 246. Accordingly, the chip connecting wiring 25 is

^{formed}
~~composed~~ of ITO as is the pixel electrode 23.

[0027] As shown in Fig. 5(a) and (b), each chip connecting wiring 25 is connected to the corresponding data line 22 via contact holes 25a formed in the third insulating layer 246. Moreover, the contact holes 25a of this embodiment are formed in the region surrounded by the outer edges of the sealing material 30, as shown in Fig. 5(a) and (b). In detail, the contact holes 25a are formed in a region comprising the portion of the third insulating layer 246 facing or contacting the sealing material 30 and the portion inside the sealing material 30 facing the liquid crystal 40. This region is hereinafter referred to as the "covered region 61".

[0028] ^{where} ~~When~~ the X driver IC 52 is mounted on the projection 201, i.e., on the third insulating layer 246 to be more precise, via an anisotropic conductive film, the conductive particles in the anisotropic conductive film connect the chip connecting wiring 25 to an output terminal 52a of the X driver IC 52. As a result, each output terminal 52a of the X driver IC 52 is electrically connected to the corresponding data line 22 via the chip connecting wiring 25, and a data signal output from the X driver IC 52 is supplied to the data line 22.

[0029] As is described above, according to this embodiment, the data lines 22 on the projection 201 are covered with the third insulating layer 246. In other words, the portions of the data lines 22 on the projection 201 as well as those surrounded by the sealing material 30 do not come into contact with the outside air, ^{hence,} ~~hence,~~ corrosion of the data lines 22 caused by moisture in the outside air can be effectively prevented. Moreover, in this embodiment, each data line 22 is connected to the corresponding output terminal 52a of the X driver IC 52 via the chip connecting wiring 25. Accordingly, the entire length of the data line 22 including the portion connected to the output terminal 52a is ^{prevented or} ~~prevented~~ from being exposed outside, and corrosion of the data lines 22 can be securely prevented.

[0030] Each chip connecting wiring 25 of this embodiment is formed in the same step as the pixel electrodes 23, ^{if} ~~if~~ accordingly, the manufacturing cost is lower compared to the case in which the chip connecting wiring 25 and the pixel electrodes 23 are manufactured in separate steps. Furthermore, since ITO is a chemically stable oxide and is resistant to

corrosion, the problem of corrosion does not occur even if a structure comprising chip connecting wiring 25 exposed to the outside air is employed, as shown in Fig. 5(a) and (b). *is employed that includes*

[0031] Moreover, in this embodiment, the contact holes 25a for connecting the chip connecting wiring 25 to the data line 22 are formed in the covered region 61 to securely prevent corrosion of the data lines 22. The details are described below.

[0032] Fig. 6(a) and (b) show a comparative example in which the contact holes 25a are formed in the region other than the covered region 61, i.e., the region outside the sealing material 30. In this structure, since the major parts of the data line 22 are covered with the third insulating layer 246 excluding the portion at the contact holes 25a, the corrosion of the data lines 22 is less significant compared to the case where no third insulating layer 246 is provided and data lines 22 are exposed at the projection 201. However, ITO constituting the chip connecting wiring 25 readily allows moisture to permeate. Thus, the moisture contained in the outside air reaches the data lines 22 through the contact holes 25a, thereby corroding the data lines 22. *that*

[0033] In contrast, the contact holes 25a of this embodiment are formed inside the covered region 61 covered with the sealing material 30 and the liquid crystal 40. The chip connecting wiring 25 in the vicinity of the contact holes 25a does not come in contact with the outside air. Thus, moisture is prevented from reaching the data line 22 through the contact holes 25a. In other words, according to this embodiment, the corrosion of the data lines 22 can be securely avoided compared with the case shown in Fig. 6(a) and (b). *reduced or*

<B: Second Embodiment>

[0034] Next, a second embodiment of the present invention is described. A liquid crystal display of this embodiment has the same structure as that of the liquid crystal display 1 of the first embodiment except for the configuration of the vicinity of the projection 201. In the description below, only the structure of the vicinity of the projection 201 is described according to this embodiment, and descriptions of other components are omitted.

[0035] Fig. 7(a) is a plan view showing the structure of the vicinity of the projection 201 of the liquid crystal display of this embodiment. Fig. 7(b) is a cross-sectional view taken along line D-D' in Fig. 7(a). As shown in these drawings, the liquid crystal *plane* *Figs. 7(a) and 7(b)*

display of this embodiment is similar to the liquid crystal display 1 of the first embodiment in that each data line 22 extends in the Y direction from the region surrounded by the sealing material 30 onto the projection 201 and that the major portion of each data line 22 is covered with the third insulating layer 246. The difference lies in the state of connection between the data line 22 and the output terminal 52a of the X driver IC 52.

[0036] To be more specific, as shown in Fig. 7(a) and (b), each chip connecting wiring 25 of this embodiment covers the regions of the third insulating layer 246 at which the output terminals 52a of the X driver IC 52 are formed and the vicinities of such regions. The chip connecting wiring 25 is connected to the output terminal 52a of the X driver IC 52 ^{where} the X driver IC 52 is mounted. Each data line 22 has an end thereof reaching a position in the projection 201 corresponding to the output terminal 52a of the X driver IC 52. In other words, the end portion of the data line 22 disposed on the projection 201 faces the chip connecting wiring 25 with the third insulating layer 246 therebetween.

[0037] In this structure, the chip connecting wiring 25 is electrically connected to the corresponding data line 22 via the contact holes 25a formed in the third insulating layer 246. In this embodiment, the contact holes 25a are disposed inside a region 62 of the third insulating layer 246 at which the X driver IC 52 is mounted, i.e., the region covered by the X driver IC 52. This region is hereinafter referred to as the "mounting region 62". A resin or mold seals the connecting portion between the X driver IC 52 and the chip connecting wiring 25.

[0038] In this embodiment, as in the first embodiment, the data lines 22 are covered with the third insulating layer 246 ^{to reduce or} to prevent corrosion of the data lines 22. Since the chip connecting wiring 25 and the pixel electrodes 23 can be manufactured in the same step, the manufacturing cost can be reduced. Moreover, according to this embodiment, the contact holes 25a ^{that} for connecting the chip connecting wiring 25 to the data line 22 are disposed inside the mounting region 62 ^{that} for mounting the X driver IC 52, and a resin seals the connecting portion between the output terminal 52a of the X driver IC 52 and the chip connecting wiring 25. Thus, even when the chip connecting wiring 25 is made of a material which readily permeates moisture, for example, ITO, moisture is inhibited from reaching the data lines 22

through the contact holes 25a and corrosion of the data lines 22 can be reliably ^{reduced or} prevented.

<C: Modification>

[0039] The above descriptions on embodiments are for ^{only} the illustrative purpose ^{only} and various modifications may be imparted to these embodiments without departing from the scope of the present invention. Examples of ^{the} modifications are as follows.

(1) The first embodiment illustrates a structure having the contact holes 25a disposed inside the covered region 61. The second embodiment illustrates a structure having the contact holes 25a disposed inside the mounting region 62. However, both of these structures can be employed. In particular, while forming each chip connecting wiring 25, which extends to the vicinity of the output terminal 52a of the X driver IC 52 from the interior of the sealing material 30, on the third insulating layer 246 so as to face the corresponding data line 22, the chip connecting wiring 25 may be connected to the data line 22 via the contact holes 25a formed in both the covered region 61 facing the sealing material 30 and the liquid crystal 40 and the mounting region 62 for the X driver IC 52 of the third insulating layer 246.

[0040] Moreover, in the first embodiment, the contact holes 25a are formed in the two regions of the third insulating layer 246, one facing the sealing material 30 and the other facing the liquid crystal 40. However, the contact holes 25a may be formed in only one of the two regions. The point is to form the contact holes 25a in the covered region, i.e., a first region, including a region facing the liquid crystal 40 so as to achieve a structure capable of ^{reducing or} preventing moisture in the outside air from reaching the vicinity of the contact holes 25a.

(2) Each of the above-described embodiments illustrates a structure having the X driver IC 52 which is mounted on the projection 201 by the COG method. Alternatively, a flexible substrate having an X driver IC mounted thereon may be mounted on the projection 201. In particular, a flexible substrate ^{including} comprising a film base having an X driver IC mounted thereon and wiring formed on the surface of the film base is bonded to the projection 201 through an anisotropic conductive layer. The conductive particles in the anisotropic conductive layer then electrically connect the wiring of the flexible substrate to the chip connecting wiring 25 on the projection 201. ^{where} When the flexible substrate is mounted instead of the X driver IC 52 in the structure illustrated in the second embodiment, the region of the third insulating layer

246 bonded to the flexible substrate (more specifically, the region covered with the film base) is defined as the "mounting region". Accordingly, the contact holes 25a formed in this mounting region to connect the chip connecting wiring 25 to the data line 22 prevents moisture in the outside air from reaching the vicinity of the contact holes 25a due to the film base, achieving the same advantages as in the above-described second embodiment. In view of the above, the "electronic component" in these embodiments is not limited to a driver IC ^{or instead} but includes various components having electrodes or wiring to be connected to the chip connecting wiring 25.

(3) In the first embodiment, each data line 22 extends over the projection 201. However, since the data line 22 and the chip connecting wiring 25 are connected to each other at the covered region 61, the data lines 22 need not extend over the projection 201. Moreover, although only the data lines 22 are connected to the output terminals 52a of the Y driver IC 52 through the chip connecting wiring 25 in the above-described embodiments and modifications, the same arrangement may naturally be employed for the scanning lines 21.

(4) In each of the above-described embodiments, the data lines 22 are made of aluminum and the chip connecting wiring 25 are made of ITO. However, this does not limit the material of these wiring. From the point of view of preventing corrosion of the data lines 22, the material for the chip connecting wiring 25 is preferably chemically more stable, i.e., more corrosion-resistant, than that for the data lines 22. For example, while the data lines 22 may be made of an elemental metal such as silver, copper, or chromium, or of an alloy containing these metals, the chip connecting wiring 25 may be made of SnO₂, gold, platinum, or the like, which is chemically more stable than the data lines 22. Although the chip connecting wiring 25 and the pixel electrodes 23 are formed in the same step in the above-described embodiments, they may be formed in separate steps.

(5) In the above embodiments, an active matrix liquid crystal display using TFTs as the three-terminal switching elements is illustrated. However, the present invention is applicable to an active matrix liquid crystal display using two-terminal switching elements such as thin film diodes (TFDs) or to a passive matrix liquid crystal display, which has no switching elements.

[0041] In the above-described embodiments, the present invention is applied to a liquid crystal display using liquid crystal as the electro-optical material. However, the present invention is applicable to various other devices which display images by an electro-optical effect using electroluminescent elements, such as organic electroluminescent elements, as the electro-optical material. The present invention is applicable to various other electro-optical devices as long as they have wiring on the substrate and the wiring are connected to the terminals of an electronic component.

<D: Electronic apparatus>

[0042] Next, electronic apparatuses incorporating the electro-optical device of the present invention are described.

(1) Mobile computer

[0043] A portable personal computer, i.e., a so-called notebook computer, having a display unit incorporating the electro-optical device of the present invention is now explained. Fig. 8(a) is a perspective view showing the structure of this personal computer. As shown in the drawing, a personal computer 81 ^{includes} comprises a main unit 812 including a keyboard 811; and a display unit 813 incorporating the electro-optical device of the present invention.

(2) Cellular phone

[0044] A cellular phone having a display unit incorporating the electro-optical device of the present invention is now explained. Fig. 8(b) is a perspective view showing the structure of this cellular phone. As shown in the drawing, a cellular phone 82 ^{includes} comprises a plurality of operation buttons 821, an earpiece 822, a mouthpiece 823, and a display unit 824 incorporating the electro-optical device of the present invention.

[0045] Examples of electronic apparatuses capable of incorporating the electro-optical devices of the present invention other than the personal computer and the cellular phone shown in Fig. 8(a) and (b) are liquid crystal televisions, viewfinder- or direct-view-type video recorders, car navigation systems, pagers, electronic databooks, calculators, word processors, work stations, video telephones, POS terminals, digital still cameras, and projectors using the electro-optical device of the present invention as the light valves. As is

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~~previously~~ ^{above} described, in the electro-optical device of the present invention, corrosion of the wiring on the substrate can be inhibited to ensure reliability in the performance of the electronic device incorporating the electro-optical device.

[0046] As is described above, the present invention suppresses corrosion of the wiring formed on the substrate.